

**Amendments to the Specification**

Please add the following three new paragraphs after paragraph [0016]:

[0017] Optionally in some embodiments, in case of temporary absence of timing signal slave units may continue data transmission within “their” half-cycles of network fundamental voltage computing their temporary location from a known half-cycle value of timing signal.

[0018] Optionally in some embodiments, an individual device other than a system main unit may be a source supplying a timing signal.

[0019] Optionally in some embodiments, a timing signal may be subjected to modulation and may be used for broadcast data transmission from a main unit to slave ones.

[0020] Optionally in some embodiments, all signals being transmitted by a main and slave units may have duration equal to 1/3 of a network voltage half-cycle and may be centered about zero crossing points of the fundamental harmonic of system supply line voltage.